

20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	27mΩ @ V _{GS} = -4.5V	-7.6A
-20V	32mΩ @ V _{GS} = -2.5V	-6.7A
	50mΩ @ V _{GS} = -1.8V	-5.2A
	90mΩ @ V _{GS} = -1.5V	-3.9A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- **Battery Management Application**
- **Power Management Functions**
- DC-DC Converters

Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.007 Grams (Approximate)

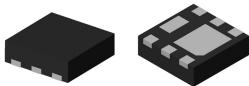
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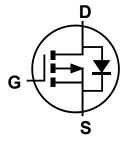


Top View

5 D S

Pin Out **Bottom View**

6 D



Internal Schematic

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel
DMP2023UFDF-7	3F	7	3,000
DMP2023UFDF-13	3F	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Bottom View

Marking Information

U-DFN2020-6



3F = Product Type Marking Code YM = Date Code Marking Y = Year (ex: B = 2014)M = Month (ex: 9 = September)

Date Code Key

Year	2014		2015	2016		2017	2018	1	2019	2020		2021
Code	В		С	D		Е	F		G	Н		
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	1	5	6	7	Ω	٥	0	N	n



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-20	V		
Gate-Source Voltage	V_{GSS}	±8	V		
Continuous Drain Current (Note 6) V - 4 EV	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-7.6 -6.1	Α
Continuous Drain Current (Note 6) V _{GS} = -4.5V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι _D	-9.5 -7.6	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I_{DM}	-40	Α		
Continuous Source-Drain Diode Current	Is	-2	Α		
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	- 23	Α		
Repetitive Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	27	mJ		

Thermal Characteristics

Characteristic	Symbol	Value	Units		
Total Dower Dissinction (Note 5)	T _A = +25°C	Б	0.73	W	
Total Power Dissipation (Note 5)	T _A = +70°C	P_{D}	0.47		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		171	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	$R_{\theta JA}$	112		
Total Dower Dissinction (Note 6)	T _A = +25°C	Б	2.03	W	
Total Power Dissipation (Note 6)	T _A = +70°C	P_{D}	1.30		
Thermal Desistance Junction to Ambient (Note 6)	Steady State	D	62	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{\theta JA}$	40		
Thermal Resistance, Junction to Case (Note 6) Steady State		$R_{ heta JC}$	9.3		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

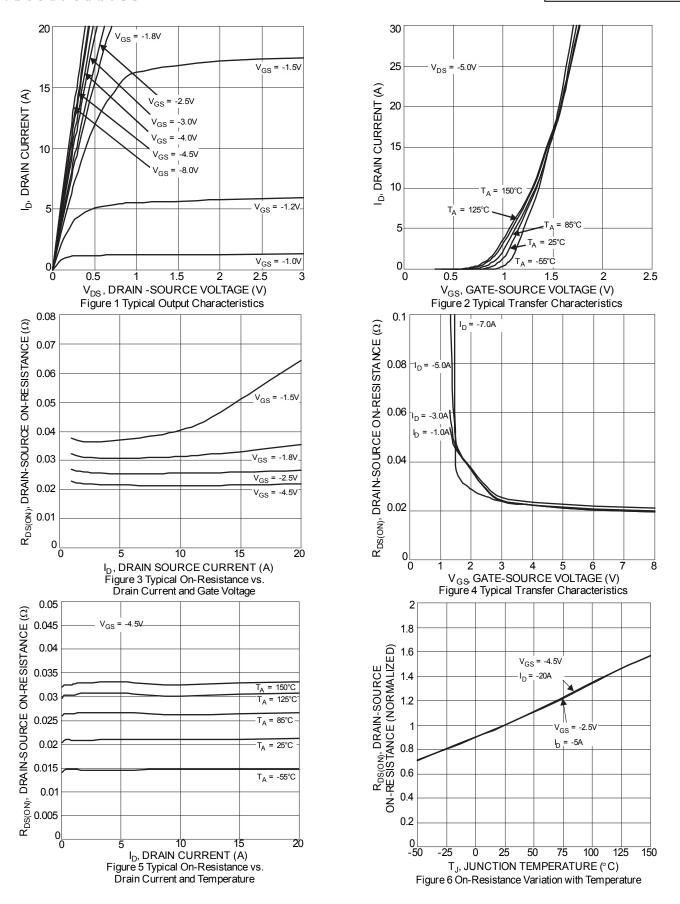
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage	BV _{DSS}	-20	1	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	1	-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	_	-	±100	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	V _{GS(th)}	-0.4	-	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		
			1	27		$V_{GS} = -4.5V$, $I_{D} = -7.0A$		
Static Drain-Source On-Resistance	D		ı	32	mΩ	$V_{GS} = -2.5V$, $I_{D} = -5.0A$		
Static Drain-Source On-Resistance	R _{DS(ON)}	_	-	50	11122	$V_{GS} = -1.8V$, $I_{D} = -3.0A$		
			1	90		$V_{GS} = -1.5V, I_D = -1.0A$		
Diode Forward Voltage	V_{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_S = -1.0A$		
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	C _{iss}	_	1837	_		V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz		
Output Capacitance	C _{oss}	_	131	_	pF			
Reverse Transfer Capacitance	C _{rss}	_	115	_		1 - 1.000112		
Gate Resistance	R_g	_	14.8	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$		
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	27	_		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
Gate-Source Charge	Q_{gs}	_	2.8	_	nC	$V_{DS} = -15V, V_{GS} = -4.5V,$ $I_{D} = -4.0A$		
Gate-Drain Charge	Q _{gd}	_	3.1	_		I _D = -4.0A		
Turn-On Delay Time	t _{D(on)}	_	5.8	_				
Turn-On Rise Time	t _r	_	19.3	_		$V_{DS} = -15V, V_{GS} = -4.5V,$		
Turn-Off Delay Time	t _{D(off)}	_	168.5	_	ns	$R_G = 1\Omega$, $I_D = -4.0A$		
Turn-Off Fall Time	t _f	_	77.3	_				
Reverse Recovery Time	t _{rr}	_	46.5	_	ns	I _F = -1.0A, di/dt = 100A/μs		
Reverse Recovery Charge	Q _{rr}	_	33.8	_	nC	I _F = -1.0A, di/dt = 100A/μs		

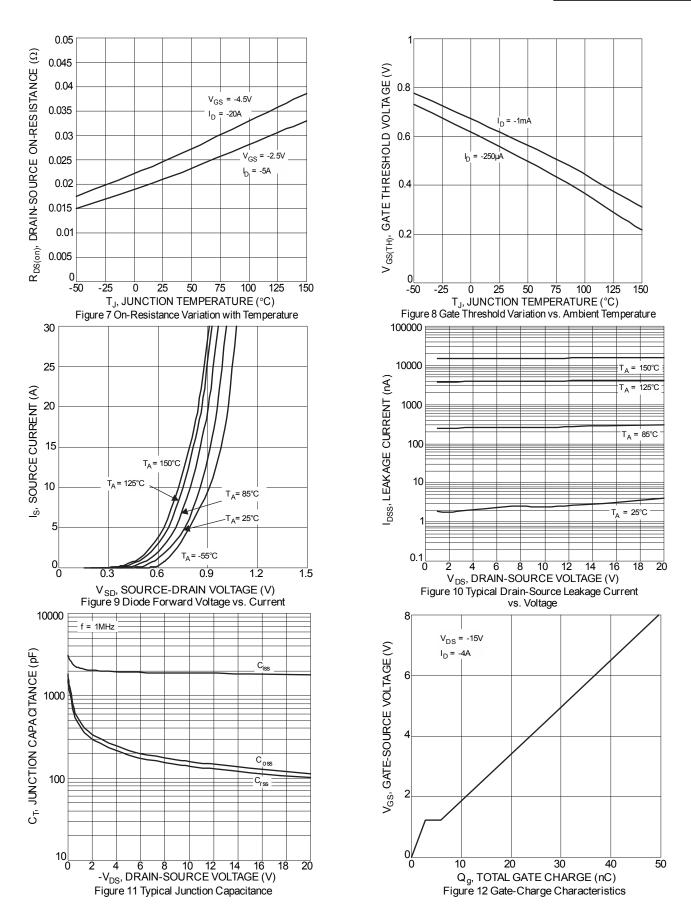
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
 Short duration pulse test used to minimize self-heating effect. Notes:

^{9.} Guaranteed by design. Not subject to product testing.

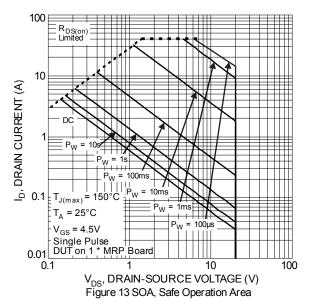


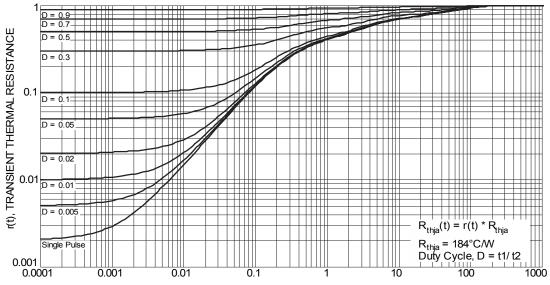










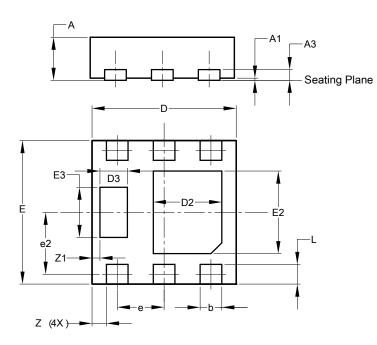


t1, PULSE DURATION TIMES (sec) Figure 14 Transient Thermal Resistance



Package Outline Dimensions

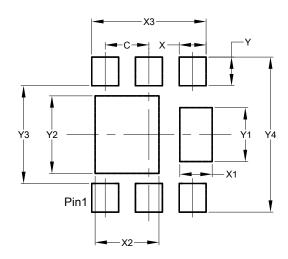
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



U-DFN2020-6								
(Type F)								
Dim	Min	Min Max Typ						
Α	0.57	0.63	0.60					
A1	0	0.05	0.03					
A3	ı	1	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D3	0.33	0.43	0.38					
е		0.65 BSC						
e2	C).863 B	SC					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E3	0.65	0.75	0.70					
L	0.225 0.325 0.275							
Z	(0.20 BSC						
Z 1	0.110 BSC							
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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